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PTO/SB/08a/b (07-05)  
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Substitute for form 1449A/B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete If Known</b>	
				Application Number	10/646,673-Conf. #1020
				Filing Date	August 21, 2003
				First Named Inventor	Kevin L. Beaman
				Art Unit	1762
				Examiner Name	David Turocy
Sheet	1	of	2	Attorney Docket Number	108298715US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
DPT		US-4,923,715	05-08-1990	Matsuda et al.	
		US-3,620,934	11-16-1971	Endle	
		US-5,020,476	06-04-1991	Bay et al.	
		US-5,366,557	11-22-1994	Yu	
		US-5,575,883	11-19-1996	Nishikawa et al.	
		US-6,206,967	03-27-2001	Mak et al.	
		US-6,251,190	06-26-2001	Mak et al.	
		US-6,689,220	02-10-2004	Nguyen	
		US-6,887,521	05-03-2005	Basceri	
DPT		US-11/115,728	Filed 04-26-2005	Qin	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
DPT		JP-63-111177-A	05-16-1988	Hitachi Ltd.		
DPT		JP-6-342785-A	12-13-1994	Fujitsu Ltd.		

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. \* CITE NO.: Those application(s) which are marked with an single asterisk (\*) next to the Cite No. are not supplied (under 37 CFR 1.98(a)(2)(iii)) because that application was filed after June 30, 2003 or is available in the IFW. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T <sup>2</sup>
DPT		Aera Corporation, "Fundamentals of Mass Flow Control," 1 page, retrieved from the Internet on March 6, 2003, < <a href="http://www.aeramfc.com/funda.shtml">http://www.aeramfc.com/funda.shtml</a> >.			
DPT		Electronic Times, "Atomic Layer Deposition Chamber Works at Low Temperatures," 2 pages, 12/11/2001, 2001 CMP Europe Ltd., < <a href="http://www.electronicstimes.com/story/OEG20010719S0042">http://www.electronicstimes.com/story/OEG20010719S0042</a> >			
DPT		EMCO Flow Systems, "Mach One Mass Flow Controllers", 1 page, retrieved from the Internet on November 7, 2003, < <a href="http://emcoflow.com/mach-one.htm">http://emcoflow.com/mach-one.htm</a> >.			

Examiner Signature	/David Turocy/	Date Considered	09/05/2006
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DPT	EMCO Flow Systems, "Mach One Mass Flow Controller Product Brochure" 6 pages, retrieved from the Internet on November 7, 2003, < <a href="http://www.emcoflow.com/literature/brochures_product_sheets/mach_one/mach_one_brochure_2_01.pdf">http://www.emcoflow.com/literature/brochures_product_sheets/mach_one/mach_one_brochure_2_01.pdf</a> >.
DPT	SemiZone, "EMCO Flow Systems Granted Patent for the Mach One Mass Flow Controller for the Semiconductor Industry (June 28, 2001), 2 pages, retrieved from the Internet on November 7, 2003, < <a href="http://www.semizone.com/news/item?news_item_id+100223">http://www.semizone.com/news/item?news_item_id+100223</a> >.
DPT	TAKAHASHI, K et al., "Process Integration of 3D Chip Stack with Vertical Interconnection," pp. 601-609, 2004 Electronic Components and Technology Conference, IEEE, June 2004.

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